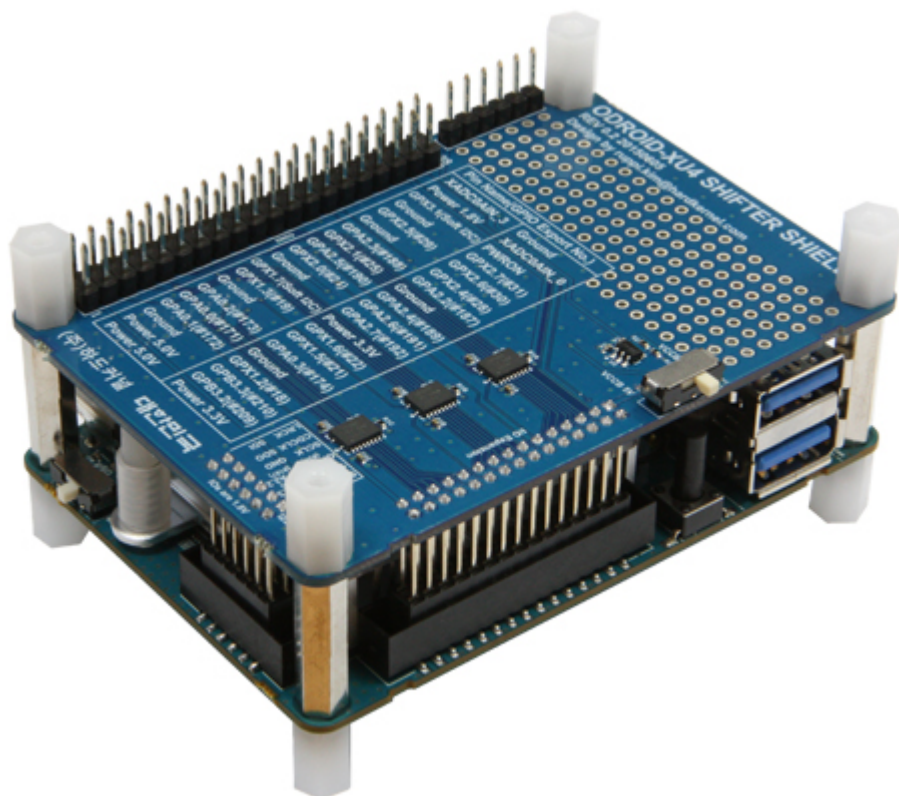


XU4 Shifter Shield



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odroid-xu4-shiftershield.fzpz

TI recommends that this current be limited to values under 1 mA for the TXS0108E. pull-down resistors must be avoided because of decreased VOH levels. If pull-down resistors are necessary they must be limited to values of **50 kΩ** or **greater**. The negative impact of pull-down resistors on VOH levels also demonstrates why the TXS family of translators must only be used to drive high-impedance loads.

[Many thanks to user qxtgo who shared this important information in our forum.](#)
[Click here for more details about pulldown/pullup resistors of shifter shield](#)

GPIO Map for WiringPi Library

Net Name	GPIO & Export No	WiringPi GPIO	Header Pin	Header Pin	WiringPi GPIO	GPIO & Export No	Net Name
3.3V	3.3V	3.3V	1	2	5.0V	5.0V	5.0V
I2C_1.SDA	GPB3.2(#209)	GPIO_08	3	4	5.0V	5.0V	5.0V
I2C_1.SCL	GPB3.3(#210)	GPIO_09	5	6	GND	GND	GND
XE.INT10	GPX1.2(#18)	GPIO_07	7	8	GPIO_15(TXD)	GPA0.1(#172)	UART_0.TXD
GND	GND	GND	9	10	GPIO_16(RXD)	GPA0.0(#171)	UART_0.RXD
UART_0.RTS_N	GPA0.3(#174)	GPIO_00	11	12	GPIO_01(PWM)	GPA0.2(#173)	UART_0.CTS_N
XE.INT13	GPX1.5(#21)	GPIO_02	13	14	GND	GND	GND

Net Name	GPIO & Export No	WiringPi GPIO	Header Pin	Header Pin	WiringPi GPIO	GPIO & Export No	Net Name
XE.INT14	GPX1.6(#22)	GPIO_03	15	16	GPIO_04	GPX1.3(#19)	XE.INT11
3.3V	3.3V	3.3V	17	18	GPIO_05	GPX1.7(#23)	XE.INT15
SPI_1.MOSI	GPA2.7(#192)	GPIO_12	19	20	GND	GND	GND
SPI_1.MISO	GPA2.6(#191)	GPIO_13	21	22	GPIO_06	GPX2.0(#24)	XE.INT16
SPI_1.SCLK	GPA2.4(#189)	GPIO_14	23	24	GPIO_10	GPA2.5(#190)	SPI_1.CSN
GND	GND	GND	25	26	GPIO_11	GPX2.1(#25)	XE.INT17
I2C_5.SDA	GPA2.2(#187)	GPIO_30	27	28	GPIO_31	GPA2.3(#188)	I2C_5.SCL
XE.INT20	GPX2.4(#28)	GPIO_21	29	30	GND	GND	GND
XE.INT22	GPX2.6(#30)	GPIO_22	31	32	GPIO_26	GPX2.5(#29)	XE.INT21
XE.INT23	GPX2.7(#31)	GPIO_23	33	34	GND	GND	GND
PWR_ON(INPUT)	PWR_ON(INPUT)	GPIO_24	35	36	GPIO_27	GPX3.1(#33)	XE.INT25
ADC_0.AIN0	AIN0(Max 1,8V)	GPIO_25	37	38	GPIO_28	ADC REF OUT(1.8V)	ADC REF OUT
GND	GND	GND	39	40	GPIO_29	AIN3(Max 1.8V)	ADC_0.AIN3

- Net Name I2C_1.SDA/I2C_1.SCL physical I2C Address : 0x12c70000
- Net Name I2C_5.SDA/I2C_5.SCL physical I2C Address : 0x12cb0000 (hsi2c)
- Net Name I2C_5.SDA/I2C_5.SCL are not available in XU3







Shifter-Shield (CON3 Header)

WiringPi GPIO#	NAME(GPIO#)				NAME(GPIO#)	WiringPi GPIO#
	3.3 V Power	1			2	5.0 V Power
8	I2C_1.SDA (#209)	3			4	5,0 V Power
9	I2C_1.SCL (#210)	5			6	Ground
7	GPIO (#18)	7			8	UART_0.TXD (#172) 15
	Ground	9			10	UART_0.RXD (#171) 16
0	UART_0.RTSN (#174)	11			12	UART_0.CTSN (#173) 1
2	GPIO (#21)	13			14	Ground
3	GPIO (#22)	15			16	GPIO (#19) 4
	3.3V Power	17			18	GPIO (#23) 5
12	SPI_1.MOSI (#192)	19			20	Ground
13	SPI_1.MISO (#191)	21			22	GPIO (#24) 6
14	SPI_1.CLK (#189)	23			24	SPI_1.CSN (#190) 10
	Ground	25			26	GPIO (#25) 11
30	I2C_5.SDA (#187)	27			28	I2C_5.SCL (#188) 31
21	GPIO (#28)	29			30	Ground
22	GPIO (#30)	31			32	GPIO (#29) 26
23	GPIO (#31)	33			34	Ground
	PWRON	35			36	GPIO (#33) 27
AIN0	ADC_0.AIN0 (ADC#0)	37			38	1.8 V Power
	Ground	39			40	ADC_0.AIN3 (ADC#3) AIN3

Attention! The WiringPi GPIO pin numbering used in this diagram is intended for use with WiringPi. The raw chipset GPIO pin numbering is "(#number)"

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Shifter-Shield (P1 Header)

WiringPi GPIO#	NAME(GPIO#)		
	3.3 V Power	1	
31	I2C_5.SCL (#188)	2	
30	I2C_5.SDA (#187)	3	
	Open	4	
	Open	5	
	Ground	6	

Attention! The WiringPi GPIO pin numbering used in this diagram is intended for use with WiringPi. The raw chipset GPIO pin numbering is "(#number)"

[Http://www.hardkernel.com](http://www.hardkernel.com)

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