

GPIO Register Map

• Please see [Expansion Connectors](#) document to check each GPIO pin locations.

GPIO Base Register	Calculated Address	GPIO Bank/Name	Register	Offset	Control Bit	Purpose	Default	Description
0xFF634400	Base Address + Offset * 4	GPIOX.[n] n = 0 to 19	PREG_PAD_GPIO2_EN_N	0x16	[19:0]	Set direction mode	1	0 to OUTPUT 1 to INPUT
			PREG_PAD_GPIO2_O	0x17		Write a value	1	0 to LOW 1 to HIGH
			PREG_PAD_GPIO2_I	0x18		Read a value	0	
			PAD_PULL_UP_EN_REG2	0x4A		Enable pull mode	0x7FFFF	0 to Disable 1 to Enable
			PAD_PULL_UP_REG2	0x3C		Set pull mode	0x5FFBF	0 to Down 1 to Up
		GPIOA.[n] n = 0 to 4, 11 to 15	PREG_PAD_GPIO5_EN_N	0x20	[15:0]	Set direction mode	1	0 to OUTPUT 1 to INPUT
			PREG_PAD_GPIO5_O	0x21		Write a value	1	0 to LOW 1 to HIGH
			PREG_PAD_GPIO5_I	0x22		Read a value	0	
			PAD_PULL_UP_EN_REG5	0x4D		Enable pull mode	0xFFFFF	0 to Disable 1 to Enable
			PAD_PULL_UP_REG5	0x3F		Set pull mode	0xC000	0 to Down 1 to Up

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