

GPIO port register map

GPIO Configuration register

Pin Number	GPIO name	Base Address	Offset	Name	Bit	Type	Description	Reset Value
15	GPX1.2	0x1340_0000	0x0C20	GPX1CON[2]	[11:8]	RW	0x0=input, 0x1=Output, 0x2=Reserved, 0x3=Reserved, 0x4=TraceData, 0x5 to 0xE=Reserved, 0xF=EXT_INT	0x00
18	GPX1.3			GPX1CON[3]	[15:12]			
13	GPX1.5			GPX1CON[5]	[23:20]			
17	GPX1.6			GPX1CON[6]	[27:24]			
25	GPX1.7			GPX1CON[7]	[31:28]			
26	GPX2.0		0x0C40	GPX2CON[0]	[3:0]			
24	GPX2.1			GPX2CON[1]	[7:4]			
20	GPX2.4			GPX2CON[4]	[19:16]			
21	GPX2.5			GPX2CON[5]	[23:20]			
19	GPX2.6			GPX2CON[6]	[27:24]			
22	GPX2.7		GPX2CON[7]	[31:28]				
27	GPX3.1		0x0C60	GPX3CON[1]	[7:4]			
CON11-5	GPX3.2	GPX3CON[2]		[11:8]				
6	GPA0.0	0x1401_0000	0x0000	GPA0CON[0]	[3:0]			
8	GPA0.1			GPA0CON[1]	[7:4]			
4	GPA0.2			GPA0CON[2]	[11:8]			
5	GPA0.3		GPA0CON[3]	[15:12]				
CON11-4	GPA2.2		0x0040	GPA2CON[2]	[11:8]			
CON11-6	GPA2.3			GPA2CON[3]	[11:8]			
10	GPA2.4			GPA2CON[4]	[19:16]			
11	GPA2.5			GPA2CON[5]	[23:20]			
9	GPA2.6			GPA2CON[6]	[27:24]			
7	GPA2.7		GPA2CON[7]	[31:28]				
16	GPB3.2		0x00C0	GPB3CON[2]	[11:8]			
14	GPB3.3			GPB3CON[3]	[15:12]			
CON11-7	GPZ.0	0x0386_0000	0x0000	GPZCON[0]	[3:0]			
CON11-9	GPZ.1			GPZCON[1]	[7:4]			
CON11-11	GPZ.2			GPZCON[2]	[11:8]			
CON11-12	GPZ.3			GPZCON[3]	[15:12]			
CON11-10	GPZ.4			GPZCON[4]	[19:16]			

GPIO Data register

GPIO name	Base Address	Offset	Name	Bit	Type	Description	Reset Value
GPX1[n]	0x1340_0000	0x0C24	GPX1DAT[7:0]	[7:0]	RWX	When you configure the port as input port, the corresponding bit is the pin state. When you configure the port as output port, the pin state is same as the corresponding bit. When you configure the port as functional pin, it reads the undefined value.	0x00
GPX2[n]		0x0C44	GPX2DAT[7:0]				
GPX3[n]		0x0C64	GPX3DAT[7:0]				
GPA0[n]	0x1401_0000	0x0004	GPA0DAT[7:0]				
GPA2[n]		0x0044	GPA2DAT[7:0]				
GPB3[n]		0x00C4	GPB3DAT[7:0]				
GPZ[n]		0x0386_0000	0x0004				

GPIO Pull-up/down register

GPIO name	Base Address	Offset	Name	Bit	Type	Description	Reset Value
GPX1[n]	0x1340_0000	0x0C28	GPX1PUD[n]	[2n+1:2n] n = 0 to 7	RW	0x0=Disables pull-up/down, 0x1=Enables pull-down, 0x2=Reserved, 0x3=Enables pull-up	0x5555
GPX2[n]		0x0C48	GPX2PUD[n]				
GPX3[n]		0x0C68	GPX3PUD[n]				
GPA0[n]	0x1401_0000	0x0008	GPA0PUD[n]				
GPA2[n]		0x0048	GPA2PUD[n]				
GPB3[n]		0x00C8	GPB3PUD[n]				
GPZ[n]		0x0386_0000	0x0008				GPZPUD[n]

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Last update: **2018/09/21 06:57**

